

Current Compensators for Unbalanced Electric Distribution Systems

Vinson Jones and Juan Carlos Balda

Department of Electrical Engineering
University of Arkansas at Fayetteville
Fayetteville, Arkansas, USA

email: vinson.j.jones@gmail.com, jbalda@uark.edu

Rambabu Adapa

Electric Power Research Institute
Palo Alto, CA
email: radapa@epri.com

Abstract— Inherent current imbalances are often present in electric distribution systems due to the increase of single-phase generation in the form of renewables and the existence of single-phase loads. The continued expansion of non-linear load usage is also increasing the levels of harmonics through the power transformers servicing these distribution systems. The issues that arise from these operating conditions are widely known and standard solutions used by utilities are as well. However, they are often bulky and do not provide a level of control or versatility appropriate for these challenges. This paper gives an overview of many of the problems that are faced on distribution systems and how an active shunt compensator may be used to mitigate or eliminate them.

Keywords— *grid-connected converter, power electronics, unbalance compensation*

I. INTRODUCTION

Distribution systems normally operate under certain amount of unbalanced loading, especially for residential systems, due to single-phase loads. The issue is exacerbated by single-phase generation in the form of rooftop solar which has seen significant increase in certain areas. Though large-scale three-phase solar generation remains by and large the majority of solar generation this does not eliminate the unbalanced nature of the loading. The issues associated with the negative- and zero-sequence current resulting from unbalanced loads are well documented [1-6]. For example: overheating of a generator, improper sizing of neutral conductors, MMF ripples inside the generator due to negative-sequence MMFs and overheating of the holding tank for ungrounded wye-connected transformers. Efficiency of the total system is also adversely affected [7, 8].

One of the most prominent issues is the voltage profile along the feeder. Utilities use load rebalancing, load-tap changers, and reactive FACTS devices to meet voltage standards. At the three-phase feeder level, there is extensive research into techniques that can be used to optimize tap scheduling and the allocation of load among the available feeders, many of which are summarized in [9]. These algorithms are used to optimize a set of variables, not just load distribution, and many do not consider the effects of load imbalance which can greatly affect the analysis [10]. They also rely heavily on the prediction of the loading curve. This can be difficult when trying to design for new load centers, especially in rural areas [11, 12].

FACTS devices are currently used to augment the existing transmission systems; however, the use of power-electronics-based FACTS at the distribution level is still in its infancy. In this paper, the application of a shunt “unbalanced current static compensator” (UCSC) will be evaluated with respect to (1) the issues mentioned above, (2) a modernizing grid with increasing distributed generation, and (3) an expanding interest in a microgrid-based distribution system.

The remainder of this paper is organized as follows: the main and secondary applications of a shunt compensator are presented in sections II and III, respectively. Simulation results and main implementation challenges for the proposed compensator are given in sections IV and V. Last thoughts are given in section VI.

II. PRIMARY APPLICATIONS OF A SHUNT COMPENSATOR

Reliability of service is a crucial criterion because customers expect consistent and limitless power availability. This requires a certain amount of margin between the nominal load and maximum feeder ratings at any given time to allow for contingencies. Unbalanced load currents add another level of complexity to the problem because any optimization effort needs to consider each phase of the feeder separately to achieve the true system-level optimal.

An UCSC installed near a distribution substation is shown in Fig. 1. In this configuration on a traditional feeder, a shunt compensator can balance the currents seen by the substation, bring unity power factor (leveraging existing shunt capacitor banks), potentially dampen power oscillations during transient events, and compensate for harmonic currents that are produced by non-linear loads [13, 14].

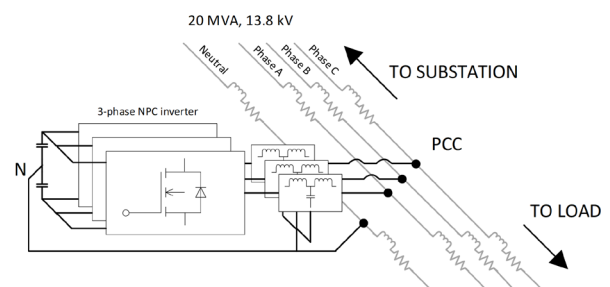


Figure 1. Connection of the UCSC to a three-phase feeder

A. Current Balancing

When each phase of the feeder is carrying different and varying currents the UCSC can draw additional current from the less loaded phases and circulate that current onto phases that are more heavily loaded. This forces the current seen upstream to the average of the three phases, helping to eliminate any upstream fundamental frequency neutral current. This improves the efficiency of electric machines connected to the grid, can improve the efficiency of the system, and can improve the voltage balance at the point of common coupling by balancing the voltage drop across the upstream impedance [4].

Around 7.7% of billable energy was lost in transmission and distribution systems in the United States in 2016 [15]. Using the average retail price that implies approximately \$31.5 billion dollars in lost revenue. Taking a transmission line with equal line resistance and an average current unbalance term, γ , the three-phase currents can be defined as:

$$I_{abc,rms} = \begin{bmatrix} I_{avg}(1 + \gamma) \\ I_{avg}(1 - \gamma) \\ I_{avg} \end{bmatrix}, \quad 0 \leq \gamma \leq 1 \quad (1)$$

where I_{avg} is the average RMS current of the phases. The proportional energy loss in the transmission line can be expressed as:

$$E_{3\phi} \propto I_{avg}^2(2\gamma^2 + 3) \quad (2)$$

In Fig. 2 the lost revenue due to transmission losses is plotted against varying γ for the cases in which transmission line losses amount to 1%, 2%, and 3% of the total billable energy. For the sake of argument, transmission lines were assumed to be operating at a γ of 0.25 for the 7.7% losses quoted above.

These calculations are simple averages that do not reflect the varying nature of loading or \$/MWh for the electric power grid, but it does reflect the potential savings for utilities that experience unbalanced loading conditions.

B. Power Factor Regulation

Regulating the power factor to unity helps to improve the total system efficiency by reducing losses accrued in the upstream transmission lines through reactive current reduction. When used in conjunction with relatively cheap and well-known switched shunt capacitor banks the rating of the UCSC can be significantly reduced. While the shunt capacitor banks compensate for most of the reactive power they can only be switched in units of around 50 kVAR per phase [16]. The UCSC can provide nearly infinite steps of reactive power within a smaller range. This

- maintains unity power factor at all operating points,
- reduces potentially the number of operations for the switched capacitor bank and thus, reduces the number transient events from the changing taps in a capacitor bank, and
- can decrease any variations in the voltage profile of the feeder.

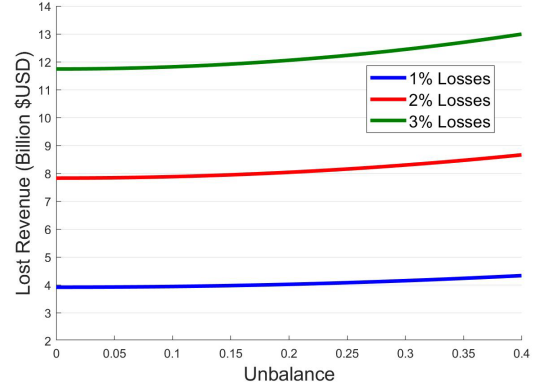


Figure 2. Lost revenue due to unbalances

This operation could be extended to a fault ride-through type operation where the UCSC remains active and provides VAR support for the grid [17].

C. Harmonic Compensation

The UCSC can eliminate by compensating for harmonic currents the voltage distortion that would otherwise propagate back into the transmission system and other feeders. Harmonic voltages can overheat shunt capacitors (decreasing their lifetime), cause increased distortion in other grid-connected converters that are not equipped with harmonic voltage compensation [18], and could excite grid resonant frequencies [16]. Harmonics currents can also over-heat power transformers, creating hot-spots and deteriorating the insulation which reduces the lifetime of the transformer [19].

III. SECONDARY APPLICATIONS OF A SHUNT COMPENSATOR

In addition to the direct benefits discussed above, there are several secondary benefits as listed below:

A. Complements Load Redistribution Practices

By extending the operating range of the upstream equipment, the conditions for load reconfiguration algorithms are laxer and thus provides more flexibility in the solution. This can help to reduce the number of reconfigurations. Currently, utilities manage current imbalances by first attempting to predict the growth of new loads and selectively add new single-phase loads, and secondly, mitigate any imbalances that occur later by moving single-phase loads to other phases as needed. This type of solution decreases the imbalance of the system but does not provide a fine-tuning of the resulting currents, because it does not account for the intermittency of the loads. The presence of the UCSC can extend the amount of time between load reconfigurations. The UCSC also maintains low neutral current at the substation for any intermittent loads. In addition, there may be instances in which there is a fault on one of the loads and a downstream fuse may open. This could cause a significant change in the loading of any phase resulting in an imbalance for which the utility cannot anticipate.

B. Inter-feeder Current Sharing

Multiple UCSCs installed at a bus that services several feeders can share a dc-link, providing current redistribution between adjacent substation transformers as in Fig. 3. In this scenario, the total power available to a feeder is no longer limited absolutely by the rating of the transformer servicing it, but by the combined rating of its UCSC and transformer. Or, to approach the problem from a more practical direction, the stress across a transformer can be reduced from near-peak conditions to help prolong its lifetime [20].

If there is a three-phase fault on Feeder #2 and a large current begins to flow through T2, instead of the UCSC on Feeder #2 deactivating, the linked UCSCs can help to distribute some of the current stress of T2 to T1.

C. Microgrid Augmentation

In the context of feeders used as microgrids, the UCSC helps to decouple the effects of loads in any one microgrid on the system during grid-connected mode, and in islanded mode the UCSC can operate as a significant STATCOM resource that can help with voltage regulation and sub-synchronous resonances [21]. The compensation capability of the UCSC needs not be limited to the currents directly upstream from the point of installation. Tertiary level controllers for the microgrid can dictate the current references for the UCSC to achieve the goals at other nodes of interest.

D. Compact Installations

HV SiC devices enable direct connection to the medium voltages of distribution feeders with significantly less complexity. This eliminates the need for low-frequency transformers and the increased achievable switching frequencies allow for a large reduction in passive components in the power-stage of the device. This enhances the modularity of the UCSC and lends itself to the possibility of incrementally adding UCSC resources to new and existing installations as dictated by load growth or changing optimization criterion for the distribution system.

Component volume reduction and elimination of the low-frequency transformer is important for the cost of the converter, but also for the cost of installation. In [16] the cost of new substation installations in 2008 were as low as \$36/kW or as high as \$110/kW, so being able to eliminate the need for new installations or to be able to pole-mount a FACTS device could remove much of these secondary costs.

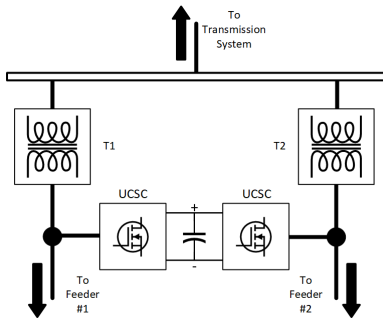


Figure 3. DC-link sharing between UCSC on adjacent feeders

E. Energy Storage and Delivery

Though conceived as an energy circulating device, if the UCSC was connected to energy storage or generation it can, with minor changes to its control criterion, supply energy to the load in addition to its other objectives. In the case of energy storage, the UCSC can be used as a peak-shaving device for high-load conditions by storing energy during times of lower cost. And in the presence of renewables like solar and wind this can be used to control the intermittency of loads seen upstream from the point of installation [22].

IV. UCSC SIMULATION RESULTS

The abilities of the UCSC to balance currents and compensate for power factor are demonstrated Fig. 4 using the control strategy presented in [23] with the UCSC connected directly to a 13.8 kV feeder just downstream from a substation. At $t = 0.05$ s the UCSC starts to compensate for the reactive component of the load and the current magnitudes reduce accordingly. The power factor is improved from 0.88 lagging to 1.0. At $t = 0.15$ s current balancing operation begins and the currents of each phase are all brought to the same magnitude. The Unbalance Factor is reduced from 23.5% to 0.15%, and defined by:

$$UBF = \frac{|negative\ sequence|}{|positive\ sequence|} \times 100\% \quad (3)$$

Though the merits of the shunt compensator for a traditional feeder are firmly established, their place in a microgrid-based distribution system with significant generation and bi-directional power flows is not as obvious. One can consider a large solar plant installed at the end of the feeder in Fig. 1. At times there may be enough generation on the feeder itself that power flow reverses in one, two, or three phases. The UCSC can still balance the current seen by the substation up to the point and past where the averaged three-phase power flow has reversed. Figure 4 shows the UCSC operating while connected to an unbalanced load while three-phase generation at the end of the feeder increases starting at $t = 0.25$ s, eventually reversing real power flow in one phase.

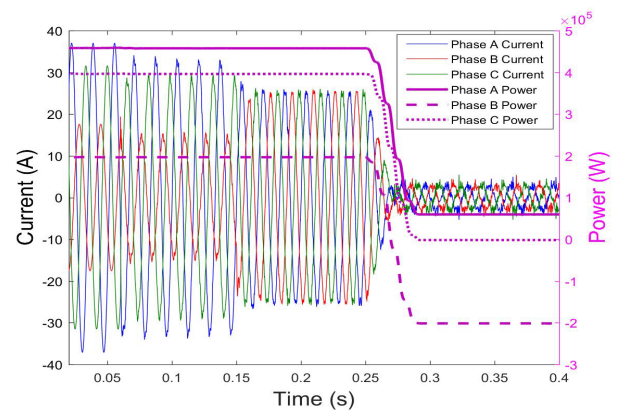


Figure 4. Substation currents and real power demand during UCSC and solar farm operation

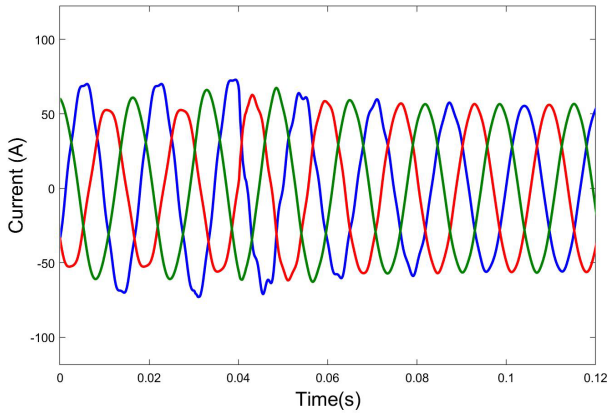


Figure 5. Substation currents before and after harmonic compensation

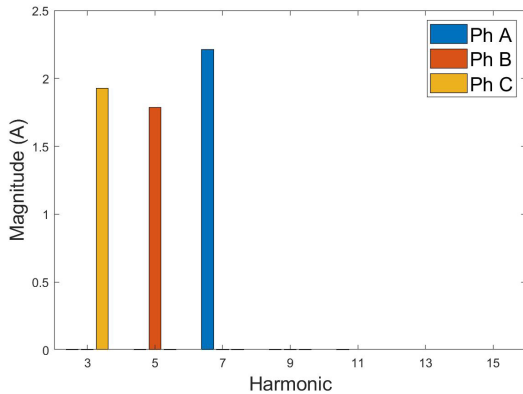


Figure 6. Substation harmonics before compensation

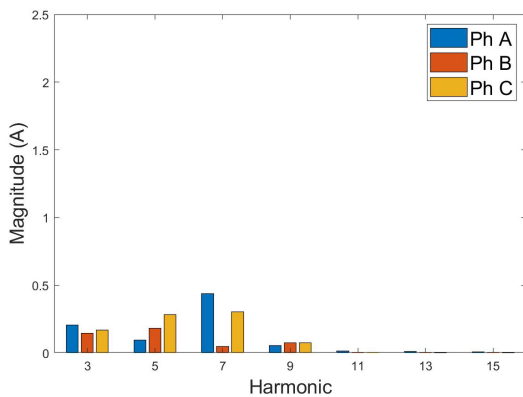


Figure 7. Substation harmonics after compensation

The UCSC acting as an active filter for harmonics is shown in Fig. 5, Fig. 6, and Fig. 7 using the control strategy proposed in [24]. The UCSC in Fig. 5 activates at $t = 0.03$ s and begins compensation at $t = 0.04$ s. Figure 6 shows the harmonic content of each substation phase current before compensation begins. The substation harmonics after imbalance and harmonic compensation begins is illustrated in Fig. 7. The 7th harmonic was reduced from 2.2 A to 0.4 A in Phase A. The 5th

harmonic was reduced from 1.8 A to 0.2 A in Phase B. And the 3rd harmonic was reduced from 1.9 A to 0.2 A in Phase C. The ability of an active filter to eliminate harmonics is a function of the switching frequency, sampling frequency, and computational power [25, 26].

V. UCSC TECHNICAL CHALLENGES

Aside from the issues that any power-electronic-based solution faces (e.g., stability, THD, efficiency), any power electronic solution for the grid faces the same issues of any piece of passive utility equipment: (1) grid transients and (2) internal faults due to these transients. Semiconductor devices do not have the same overvoltage and overcurrent capabilities of traditional utility equipment. Thus, the protection of power electronic based equipment is a prominent research topic. Fortunately, as a supplementary and shunt-connected device, the UCSC avoids many issues that would be faced by equipment like a solid-state substation [27] such as providing fault current in the event of a grid fault and accompanying voltage-sag since the UCSC can quickly deactivate or compensate and wait for the grid to recover.

A. Over-Voltage Events

Significant voltage swells can occur on the grid and these types of events can destroy power electronics if the voltage across any of the semiconductor devices rises above their breakdown limit. A worst-case scenario is lightning striking the distribution line near the UCSC. Reference [27] evaluates protections against the lightning strikes. These can include MOVs, solid-state circuit breakers, fault-current limiters, and ac crowbars. Reference [28] evaluates the selected grounding method of a grid-connected converter and its filter components for protection of the converter against lightning strikes using the IEEE 60060 1.2/50 μ s impulse voltage to approximate the lightning strike on the feeder. With these various solutions there are tradeoffs between cost, voltage surge levels, current surge levels, and any effect the solution may have on the grid.

B. Internal Faults

There are more points of failure due to the complexity and component count of these converters. An important area of research is in improving reliability and providing the ability to ride-through the fault of semiconductor devices or capacitors in the converter. Reference [29] outlines the use of redundant switching cells in modular multilevel converters (MMC) to improve reliability in the event of a device failing open or short. Reference [30] evaluates the ability of the flying capacitor converter (FCC, FCMLC) and presents solutions for continued operating after switch faults. "Hot-swapping" of submodule units in multilevel converters like in [31] paired with redundant switches or switch over-ratings can also enable smaller or eliminate down-time for power electronic equipment.

Another point of failure in most converters is the capacitor. Electrolytic capacitors can be a major limiting factor when it comes to the lifetime of the converter. Reference [32] addresses the reliability of capacitor banks based on electrolytic and film type capacitors. Film capacitors generally have greater reliability than electrolytic capacitors, but a lower

energy density leading to an inherent tradeoff between cost, size, and reliability. However, the higher switching frequencies enabled by SiC devices can shrink the capacitance required in some converters, which can make this trade-off optimization effort easier.

Though SiC is an enabling technology for direct MV connection, it brings new cabinet- and PCB-level design challenges. Powering gate drive circuitry at 10-30 kV bias with dv/dt of 100 kV/ μ s requires careful design of power supplies [33, 34]. Self-powered gate driving architectures may also be a suitable solution [35]. The selection of isolation materials is also important [36]. Reference [31] uses a solid polyurethane dielectric for a reduction in direct clearance between components, and thus converter volume, and as mechanical support for each submodule in the MMC.

VI. CONCLUSIONS

The use of an Unbalanced Current Static Compensator as a shunt compensator was evaluated for its use in electric distribution systems. The UCSC's primary functions as current balancing and power-factor correcting device were shown in simulations of a 13.8 kV feeder. The UCSC was able to achieve negligible neutral current and unity power factor for each phase. In addition, simulations of the UCSC acting as an active filter for harmonic currents were also included. The amplitudes of the 3rd, 5th, and 7th harmonics were reduced by around 80%. Potential secondary functionality of the UCSC and their benefits to the distribution system and the utility were discussed.

Current challenges associated with the implementation of grid-connected power electronics and more specifically multi-level converter structures based on SiC were presented along with solutions that have been proposed to solve these problems. This shows that the underlying technologies required to make a medium-voltage active compensator plausible exist. Though further research into reliability, cabinet-level design and proof-of-concept testing at medium-voltages approaching the 15 kV-class distribution voltages like in [37] are needed to increase confidence in the viability of FACTS implemented at the distribution system level.

ACKNOWLEDGMENTS

The authors are grateful for initial financial support from the NSF I/UCRC Grid-Connected Advanced Power Electronic Systems (GRAPES) under grant IIP-1439700. The authors are also grateful for the financial support through a tailored collaboration project between EPRI, Southern Company and Arkansas Electric Cooperative Corp.

REFERENCES

[1] A. P. S. Meliopoulos, J. Kennedy, C. A. Nucci, A. Borghetti, and G. Contaxis, "Power distribution practices in USA and Europe: impact on power quality," in *8th International Conference on Harmonics and Quality of Power. Proceedings (Cat. No.98EX227)*, 1998, vol. 1, pp. 24-29 vol.1.

[2] R. G. Harley, E. B. Makram, and E. G. Duran, "The effects of unbalanced networks on synchronous and asynchronous machine transient stability," *Electric Power System Research*,

[3] W. J. Lee, T. Y. Ho, J. P. Liu, and Y. H. Liu, "Negative sequence current reduction for generator/turbine protection," in *Conference Record of the 1993 IEEE Industry Applications Conference Twenty-Eighth IAS Annual Meeting*, 1993, pp. 1428-1433 vol.2.

[4] R. H. Salim, R. A. Ramos, and N. G. Bretas, "Analysis of the small signal dynamic performance of synchronous generators under unbalanced operating conditions," in *IEEE PES General Meeting*, 2010, pp. 1-6.

[5] B. N. Gafford, W. C. Duesterhoeft, and C. C. Mosher, "Heating of Induction Motors on Unbalanced Voltages," *Transactions of the American Institute of Electrical Engineers. Part III: Power Apparatus and Systems*, vol. 78, no. 3, pp. 282-286, 1959.

[6] T. A. Short, *Electrical Power Distribution Handbook*. Boca Raton, FL: CRC Press Taylor & Taylor Group, 2004.

[7] R. M. C. L.F. Ochoa, A. Padilha-Feltrin, G.P. Harrison, "Evaluation of distribution system losses due to load unbalance," presented at the 15th Power Systems Computation Conference PSCC 2005, Liège, Belgium, 2005-08-22, 2005.

[8] T. H. Chen, "Evaluation of line loss under load unbalance using the complex unbalance factor," *IEE Proceedings - Generation, Transmission and Distribution*, vol. 142, no. 2, pp. 173-178, 1995.

[9] L. Tang, F. Yang, and J. Ma, "A survey on distribution system feeder reconfiguration: Objectives and solutions," in *2014 IEEE Innovative Smart Grid Technologies - Asia (ISGT ASIA)*, 2014, pp. 62-67.

[10] W. Jin-Cheng, C. Hsiao-Dong, and G. R. Darling, "An efficient algorithm for real-time network reconfiguration in large scale unbalanced distribution systems," *IEEE Transactions on Power Systems*, vol. 11, no. 1, pp. 511-517, 1996.

[11] S. W. Heunis and R. Herman, "A load reconfiguration algorithm for optimizing LV residential feeders with voltage performance as the criterion," in *2001 Power Engineering Society Summer Meeting. Conference Proceedings (Cat. No.01CH37262)*, 2001, vol. 2, pp. 1211-1214 vol.2.

[12] J. Dickert and P. Schegner, "Residential load models for network planning purposes," in *2010 Modern Electric Power Systems*, 2010, pp. 1-6.

[13] C. Li, J. Deng, and X. P. Zhang, "Coordinated design and application of robust damping controllers for shunt FACTS devices to enhance small-signal stability of large-scale power systems," *CSEE Journal of Power and Energy Systems*, vol. 3, no. 4, pp. 399-407, 2017.

[14] B. N. Singh and P. Rastgoufard, "A new topology of active filter to correct power-factor, compensate harmonics, reactive power and unbalance of three-phase four-wire loads," in *Applied Power Electronics Conference and Exposition, 2003. APEC '03. Eighteenth Annual IEEE*, 2003, vol. 1, pp. 141-147 vol.1.

[15] U. S. E. I. Administration. State Electricity Profiles (2016) [Online]. Available: <https://www.eia.gov/electricity/state/>

[16] T. Gonen, *Electric Power Distribution Engineering, Third Edition*. CRC Press, 2015.

[17] R. Meyer, A. Zlotnik, and A. Mertens, "Fault Ride-Through Control of Medium-Voltage Converters With LCL Filter in Distributed Generation Systems," *IEEE Transactions on Industry Applications*, vol. 50, no. 5, pp. 3448-3456, 2014.

[18] X. P. A. Gray, and P. W. Lehn, "New Metric Recommended for IEEE Standard 1547 to Limit Harmonics Injected Into

- Distorted Grids," *IEEE Transactions on Power Delivery*, vol. 31, no. 3, pp. 963-972, 2016.
- [19] L. Pierrat, R. J. Resende, and J. Santana, "Power transformers life expectancy under distorting power electronic loads," in *Proceedings of IEEE International Symposium on Industrial Electronics*, 1996, vol. 2, pp. 578-583 vol.2.
- [20] Y. Biçen, F. Aras, and H. Kirkici, "Lifetime estimation and monitoring of power transformer considering annual load factors," *IEEE Transactions on Dielectrics and Electrical Insulation*, vol. 21, no. 3, pp. 1360-1367, 2014.
- [21] R. K. Varma and R. Salehi, "SSR Mitigation With a New Control of PV Solar Farm as STATCOM (PV-STATCOM)," *IEEE Transactions on Sustainable Energy*, vol. 8, no. 4, pp. 1473-1483, 2017.
- [22] M. Black and G. Strbac, "Value of Bulk Energy Storage for Managing Wind Power Fluctuations," *IEEE Transactions on Energy Conversion*, vol. 22, no. 1, pp. 197-205, 2007.
- [23] V. Jones and J. C. Balda, "Correcting current imbalances in three-phase four-wire distribution systems," in *2016 IEEE Applied Power Electronics Conference and Exposition (APEC)*, 2016, pp. 1387-1391.
- [24] M. Castilla, J. Miret, J. Matas, L. G. d. Vicuna, and J. M. Guerrero, "Control Design Guidelines for Single-Phase Grid-Connected Photovoltaic Inverters With Damped Resonant Harmonic Compensators," *IEEE Transactions on Industrial Electronics*, vol. 56, no. 11, pp. 4492-4501, 2009.
- [25] X. Wen, Y. W. Li, and J. He, "A distribution system harmonic compensation approach using DG-grid interfacing converters at low switching frequency," in *2014 IEEE Applied Power Electronics Conference and Exposition - APEC 2014*, 2014, pp. 995-1001.
- [26] F. Blaabjerg, R. Teodorescu, M. Liserre, and A. V. Timbus, "Overview of Control and Grid Synchronization for Distributed Power Generation Systems," *IEEE Transactions on Industrial Electronics*, vol. 53, no. 5, pp. 1398-1409, 2006.
- [27] T. Guillod, F. Krismer, and J. W. Kolar, "Protection of MV Converters in the Grid: The Case of MV/LV Solid-State Transformers," *IEEE Journal of Emerging and Selected Topics in Power Electronics*, vol. 5, no. 1, pp. 393-408, 2017.
- [28] J. Carr, W. Zhenyuan, S. Bhattacharya, and D. Patel, "Transient overvoltage rating and BIL of the transformerless intelligent power substation," in *2013 IEEE Power & Energy Society General Meeting*, 2013, pp. 1-5.
- [29] G. T. Son *et al.*, "Design and Control of a Modular Multilevel HVDC Converter With Redundant Power Modules for Noninterruptible Energy Transfer," *IEEE Transactions on Power Delivery*, vol. 27, no. 3, pp. 1611-1619, 2012.
- [30] J. Amini and M. Moallem, "A Fault-Diagnosis and Fault-Tolerant Control Scheme for Flying Capacitor Multilevel Inverters," *IEEE Transactions on Industrial Electronics*, vol. 64, no. 3, pp. 1818-1826, 2017.
- [31] D. Cottet *et al.*, "Integration technologies for a medium voltage modular multi-level converter with hot swap capability," in *2015 IEEE Energy Conversion Congress and Exposition (ECCE)*, 2015, pp. 4502-4509.
- [32] V. Najmi, J. Wang, R. Burgos, and D. Boroyevich, "High reliability capacitor bank design for modular multilevel converter in MV applications," *2014 IEEE Energy Conversion Congress and Exposition (ECCE)*, pp. 1051-1058, 2014.
- [33] V.-S. Nguyen, L. Kerachev, P. Lefranc, and J.-C. Crebier, "Characterization and Analysis of an Innovative Gate Driver and Power Supplies Architecture for HF Power Devices With High dv/dt," *IEEE Transactions on Power Electronics*, vol. 32, no. 8, pp. 6079-6090, 2017.
- [34] B. Sarrazin, R. Hanna, P. Lefranc, S. Am, F. Dumas, and J. P. Lavieville, "Insulated power supply for gate drivers up to 40kV for medium-voltage direct current applications," *IET Power Electronics*, vol. 10, no. 15, pp. 2143-2148, 2017.
- [35] J. C. Crebier and N. Rouger, "Loss Free Gate Driver Unipolar Power Supply for High Side Power Transistors," *IEEE Transactions on Power Electronics*, vol. 23, no. 3, pp. 1565-1573, 2008.
- [36] P. Wang, G. C. Montanari, and A. Cavallini, "Partial Discharge Phenomenology and Induced Aging Behavior in Rotating Machines Controlled by Power Electronics," *IEEE Transactions on Industrial Electronics*, vol. 61, no. 12, pp. 7105-7112, 2014.
- [37] S. Madhusoodhanan *et al.*, "Performance evaluation of 15 kV SiC IGBT based medium voltage grid connected three-phase three-level NPC converter," in *2015 IEEE Energy Conversion Congress and Exposition (ECCE)*, 2015, pp. 3710-3717.